

S57	1058670	(chip or (micro near3 chip)) or ((microprocessor same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2) same (hardware) same protection)	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/26 08:10
S59	12	("5628023" "6260131" "6304944" "6351797" "6408373" "6412043" "6418523" "6430664" "6430670").PN. OR ("6643759").URPN.	US-PGPUB; USPAT; USOOR	AND	ON	2008/08/26 08:12
S61	1	"10536732"	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT	AND	ON	2009/06/10 09:09
S62	2	("2003/0014653").URPN.	USPAT	AND	ON	2009/06/10 09:24
S63	9	(chip or (micro near3 chip) or microprocessor or processor) same (memory or register) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2 or signal\$2) same (hardware) same protection) and mips	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 11:14
S65	0	Mips near3 instruction near3 set	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 11:16
S66	535	Mips near3 instruction near3 set	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 11:16

S67	75	(Mips near3 instruction near3 set) same chip	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 11:16
S68	3	(Mips near3 instruction near3 set) near4 chip	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 11:17
S69	7	(Mips near3 instruction near3 set) near10 chip	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:36
S70	126	(chip or (micro near3 chip) or microprocessor or processor) same (memory or register) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2) same (hardware) same protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:38
S71	75	(Mips near3 instruction near3 set) same chip	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:38
S73	995	(chip or (micro near3 chip) or (smart near3 card)) same ((protection or security or secure or authorization or authentication) near3 data) and ((authorizing or authenticating or denying) same (access or reading or writing))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:38
S74	1	S73 and S71	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:38

S75	525	(chip or (micro near3 chip)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same ((protection or security or secure or authorization or authentication) near3 data)	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:39
S76	1	S75 and S71	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:39
S77	1165449	(chip or (micro near3 chip)) or (microprocessor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2) same (hardware) same protection)	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:39
S78	75	S77 and S71	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:39
S79	824	(hardware same chip same protection)	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:39
S80	1	S79 and S71	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:39
S81	4169	713/193	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT	AND	ON	2009/07/30 13:57

S82	75	(Mips near3 instruction near3 set) same chip	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 13:57
S83	2	S82 and S81	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 13:57
S84	3787	711/163	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT	AND	ON	2009/07/30 13:57
S85	2	S82 and S84	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 13:57
S86	1332	711/164	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT	AND	ON	2009/07/30 13:57
S88	1829	711/145	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT	AND	ON	2009/07/30 13:57
S89	3	S82 and S88	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 13:57
S106	4972	((protect\$3 or control or security) near2 bit\$2) same (access) same (memory)	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/17 21:56
S107	2781	((((protect\$3 or control or security) near2 bit\$2) same (access) same (memory)) and (chip or on- chip))	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/17 21:56
S108	2828	((((protect\$3 or control or security) near2 bit\$2)) and (((secure or protected) with (memory or program)) and (chip or on-chip)))	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/17 21:57

S109	4723	713/193	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/03/17 21:58
S110	111	S108 and S109	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/17 21:58
S111	742	711/101	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/03/17 21:58
S112	3	S108 and S111	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/17 21:58
S113	5688	711/103	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/03/17 21:58
S114	132	S108 and S113	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/17 21:58
S115	1870	711/104	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/03/17 21:58
S116	26	S108 and S115	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/17 21:58
S117	2517	((protection or control or security) near2 bit\$2)) and ((secure or protected) with (memory or program)) and (chip or on-chip)	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/17 21:58
S118	17	((protection or control or security) adj bit)) same ((secure or protected) with (memory or program)) same (chip or on-chip)	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/17 21:59

S119	58	((protection or control or security) adj bit)) with ((secure or protected) with (memory or program)) and (chip or on-chip)	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/17 21:59
S120	736	((protection or control or security) adj bit)) with (chip or on-chip)	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/18 07:24
S121	4724	713/193	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/03/18 07:24
S122	5	S120 and S121	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/18 07:24
S123	742	711/101	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/03/18 07:24
S124	0	S120 and S123	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/18 07:24
S125	5707	711/103	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/03/18 07:24
S126	6	S120 and S125	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/18 07:24
S127	1873	711/104	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/03/18 07:25
S128	8	S120 and S127	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/18 07:25

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